



# Product Specification

AU OPTRONICS CORPORATION

 Preliminary Specifications Final Specifications

<b>Module</b>	<b>15.6"(15.56") FHD 16:9 Color TFT-LCD with Super 3D solution</b>
<b>Model Name</b>	<b>B156HB01 V.0</b>
<b>Note</b>	<b>LED backlight with driving circuit design</b>

**Customer****Date****Checked & Approved by****Date****Approved by****Date**Buffy Chen02/19/2012**Prepared by****Date**Rod Lee02/19/2012

Note: This Specification is subject to change without notice.

**NBBU Marketing Division  
AU Optronics Corporation**



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## Record of Revision

Version and Date	Page	Old Description	New Description	Remark
0.1 2011/03/16	All	1 <sup>st</sup> Edition for Customers		
0.2 2011/04/13	All	2 <sup>nd</sup> Edition for Customers		
1.0 2011/05/24	All		Final Edition for Customers	
1.1 2011/05/26	23		7.3 Reliability Test items added.	
1.2 2012/01/30			1 <sup>st</sup> Edition for Customers	
1.3 2012/02/19			2 <sup>nd</sup> Edition for Customers	



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## 1. Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open nor modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) After installation of the TFT Module into an enclosure (Notebook PC Bezel, for example), do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.
- 12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Disconnecting power supply before handling LCD modules, it can prevent electric shock, DO NOT TOUCH the electrode parts, cables, connectors and LED circuit part of TFT module that a LED light bar build in as a light source of back light unit. It can prevent electrostatic breakdown.



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## 2. General Description

B156HB01 V0 is a Color Active Matrix Liquid Crystal Display composed of a TFT LCD panel, a driver circuit, and LED backlight system. The screen format is intended to support the 16:9 FHD, 1920(H) x1080(V) screen and 262k colors (RGB 6-bits data driver) with LED backlight driving circuit. All input signals are LVDS interface compatible.

B156HB01 V0 is designed for a display unit of notebook style personal computer and industrial machine.

### 2.1 General Specification

The following items are characteristics summary on the table at 25 °C condition:

Items	Unit	Specifications			
Screen Diagonal	[mm]	15.6"	(15.55W")		
Active Area	[mm]	344.16(H)	x 193.59(V)		
Pixels H x V		1920	x 3(RGB)	x 1080	
Pixel Pitch	[mm]	0.17925	x 0.17925		
Pixel Format		R.G.B. Vertical Stripe			
Display Mode		Normally White			
White Luminance (ILED=20mA) <b>(Note: ILED is LED current)</b>	[cd/m <sup>2</sup> ]	300	Typ. (5 points average)	255	Min. (5 points average)
Luminance Uniformity		1.25 (5 points, Max.)			
Contrast Ratio		500:1 (Typ.)			
Resolution (3D)		near equal to HD (1357 X 763).			
Response Time	[ms]	8 (Typ.)/ 16 (Max.)			
Nominal Input Voltage VDD	[Volt]	+3.3 (Typ.)			
Power Consumption	[Watt]	11W (Max.)			
Weight	[Grams]	720g (Max.)			
Physical Size <b>(Without bracket)</b>	[mm]		Min.	Typ.	Max.
		Length	359.20	359.70	360.20
		Width	209.20	209.70	210.20
		Thickness	-	-	8.0
Electrical Interface		2 Channel LVDS			
Surface Treatment		Glare			
Support Color		262K Colors ( RGB 6-bit )			
Temperature Range					
Operating	[°C]	0 to +50			
Storage (Non-Operating)	[°C]	-20 to +60			
RoHS Compliance		RoHS Compliance			



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## 2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature) :

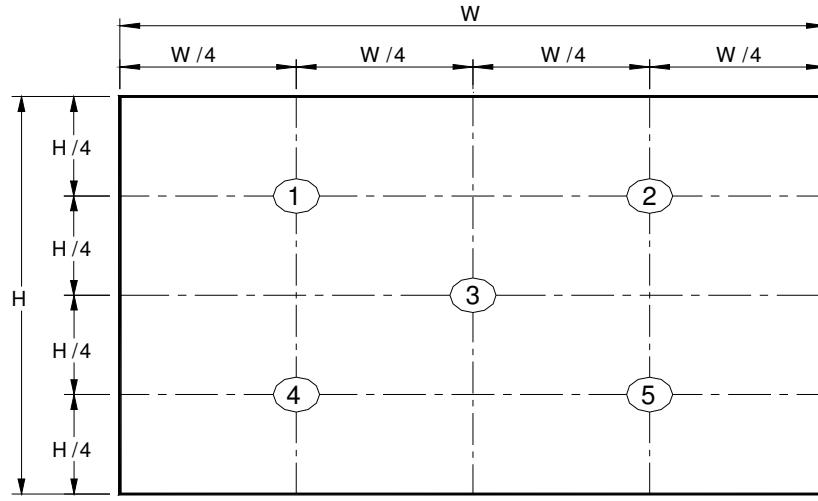
Item	Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
White Luminance ILED=20mA		5 points average	255	300	---	cd/m2	1, 4, 5.
Viewing Angle (2D)	θ <sub>R</sub> θ <sub>L</sub>	Horizontal (Right) CR = 10 (Left)	60 60	70 70	---	degree	4, 9
	ψ <sub>H</sub> ψ <sub>L</sub>	Vertical (Upper) CR = 10 (Lower)	45 50	60 60	---		
Viewing Angle (3D)	θ <sub>R</sub> θ <sub>L</sub>	Horizontal (Right) Cross talk<15% (Left)		20 20	---	degree	9,10
	ψ <sub>H</sub> ψ <sub>L</sub>	Vertical (Upper) Cross talk<15% (Lower)		15 15	---		
Viewing Distance (3D)			42		95	cm	10
Luminance Uniformity	δ <sub>5P</sub>	5 Points	---	---	1.25		1, 3, 4
Luminance Uniformity	δ <sub>13P</sub>	13 Points	---	---	1.60		2, 3, 4
Contrast Ratio	CR		400	500	-		4, 6
Cross talk (2D)	%		---	---	4		4, 7
Response Time	T <sub>RT</sub>	Rising + Falling	---	8	16	msec	4, 8
Color / Chromaticity Coodinates	Red	Rx	CIE 1931	0.650	0.620	0.590	4
		Ry		0.379	0.349	0.319	
	Green	Gx		0.354	0.324	0.294	
		Gy		0.636	0.606	0.576	
	Blue	Bx		0.182	0.152	0.122	
		By		0.148	0.118	0.088	
	White	Wx		0.283	0.313	0.343	
		Wy		0.299	0.329	0.359	
NTSC	%			-	60	-	



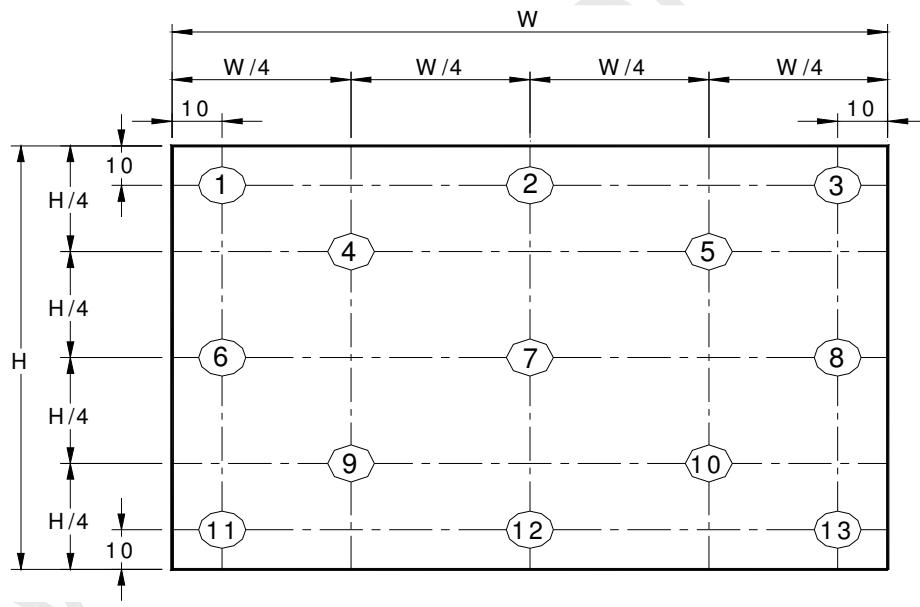
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**Note 1:** 5 points position (Ref: Active area)



**Note 2:** 13 points position (Ref: Active area)



**Note 3:** The luminance uniformity of 5 or 13 points is defined by dividing the maximum luminance values by the minimum test point luminance

$$\delta_{W5} = \frac{\text{Maximum Brightness of five points}}{\text{Minimum Brightness of five points}}$$

$$\delta_{W13} = \frac{\text{Maximum Brightness of thirteen points}}{\text{Minimum Brightness of thirteen points}}$$

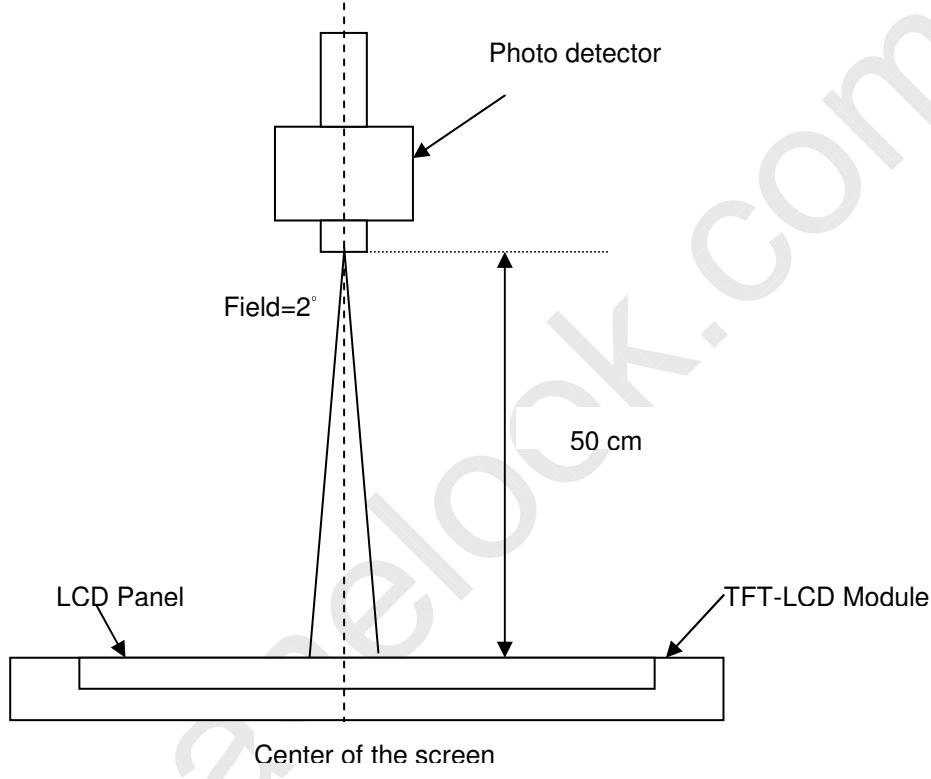


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#### Note 4: Measurement method

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room, and it should be measured in the center of screen.



#### Note 5 : Definition of Average Luminance of White ( $Y_L$ ):

Measure the luminance of gray level 63 at 5 points ,  $Y_L = [L(1)+L(2)+L(3)+L(4)+L(5)] / 5$   
 $L(x)$  is corresponding to the luminance of the point X at Figure in Note (1).

#### Note 6 : Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "White" state}}{\text{Brightness on the "Black" state}}$$



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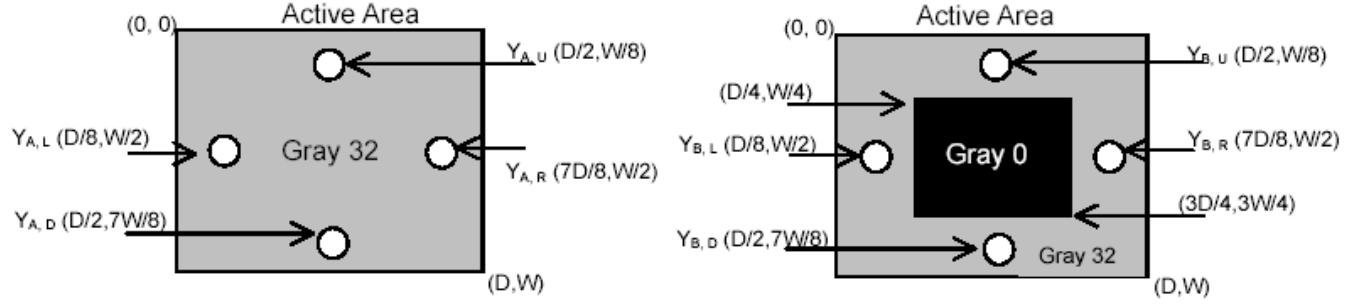
**Note 7:** Definition of Cross Talk (CT)

$$CT = | Y_B - Y_A | / Y_A \times 100 (\%)$$

Where

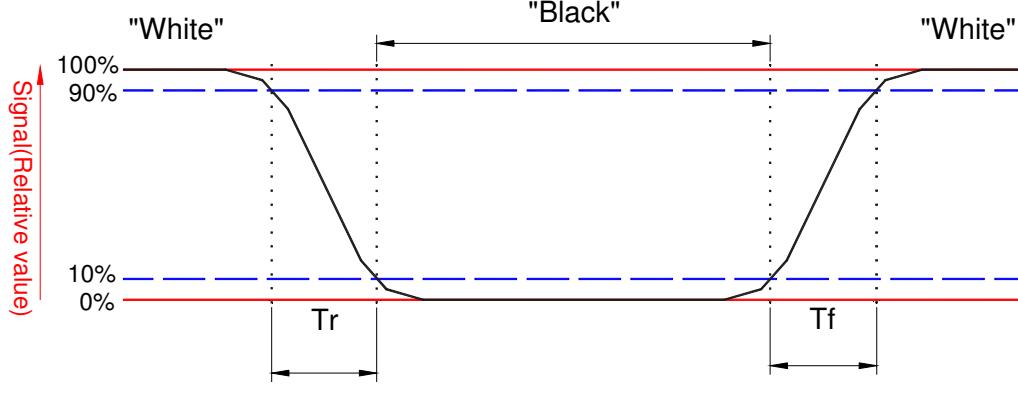
$Y_A$  = Luminance of measured location without gray level 0 pattern ( $cd/m^2$ )

$Y_B$  = Luminance of measured location with gray level 0 pattern ( $cd/m^2$ )



**Note 8:** Definition of response time:

The output signals of BM-7 or equivalent are measured when the input signals are changed from "Black" to "White" (falling time) and from "White" to "Black" (rising time), respectively. The response time interval between the 10% and 90% of amplitudes. Refer to figure as below.



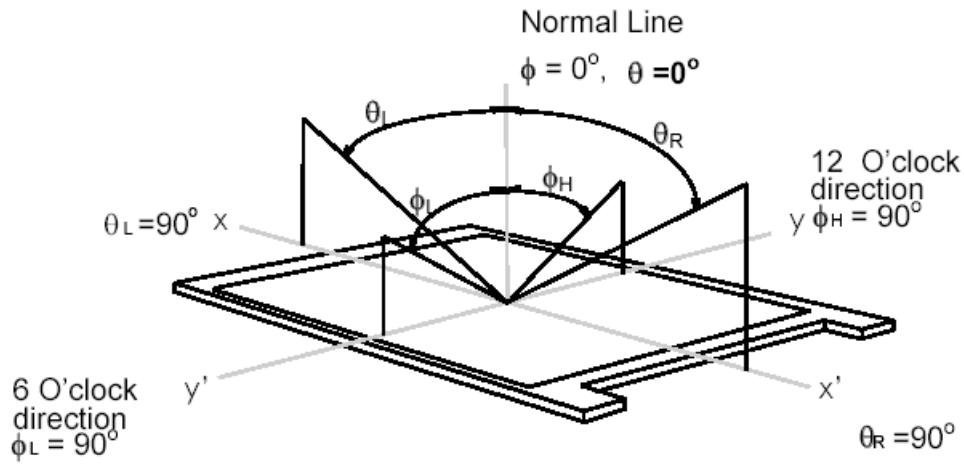


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## Note 9. Definition of viewing angle

Viewing angle is the measurement of contrast ratio >10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° ( $\theta$ ) horizontal left and right and 90° ( $\phi$ ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.



Crosstalk above for viewing angle under 3D mode is defined as below:

$$\text{Crosstalk\_Left}(\%) = \frac{L_{\text{black}}R_{\text{white}}}{L_{\text{white}}R_{\text{black}}}$$

Where

**Crosstalk\_Left(%)** means left eye crosstalk;

**L<sub>black</sub>** means left eye black signal;

**R<sub>white</sub>** means right eye white signal;

**L<sub>white</sub>** means left eye white signal;

**R<sub>black</sub>** means right eye black signal;

Right eye crosstalk is defined by analogy.

## Note10:

3D performance will be optimized by human eyes distance, panel, and webcam, SDK setting combination.

AUO provide a common value for customer to design and test.

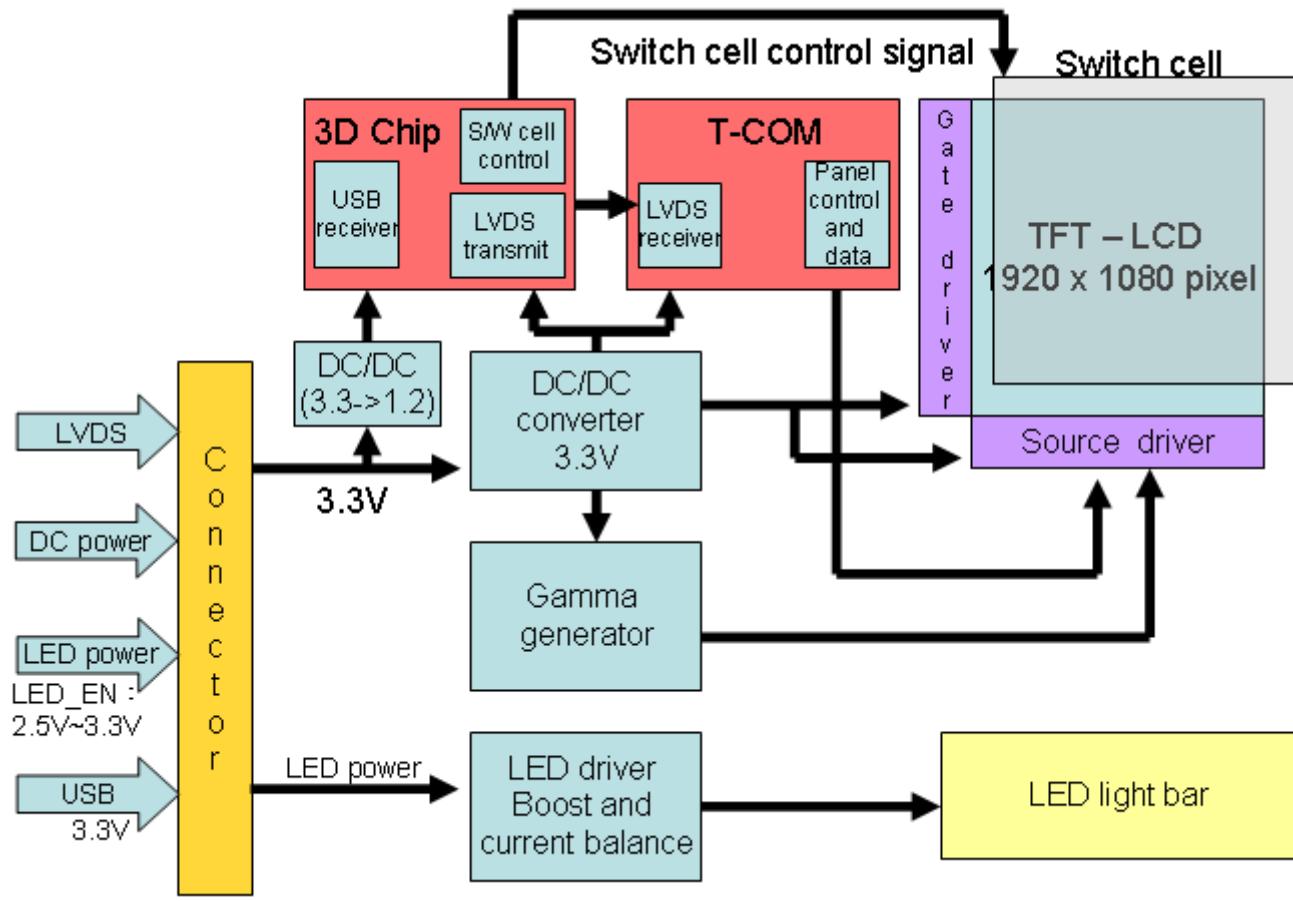


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## 3. Functional Block Diagram

The following diagram shows the functional block of the 15.6 inches wide Color TFT/LCD 40 Pin one channel Module.





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## 4. Absolute Maximum Ratings

An absolute maximum rating of the module is as following:

### 4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive	Vin	-0.3	+4.0	[Volt]	Note 1,2

### 4.2 Absolute Ratings of Environment

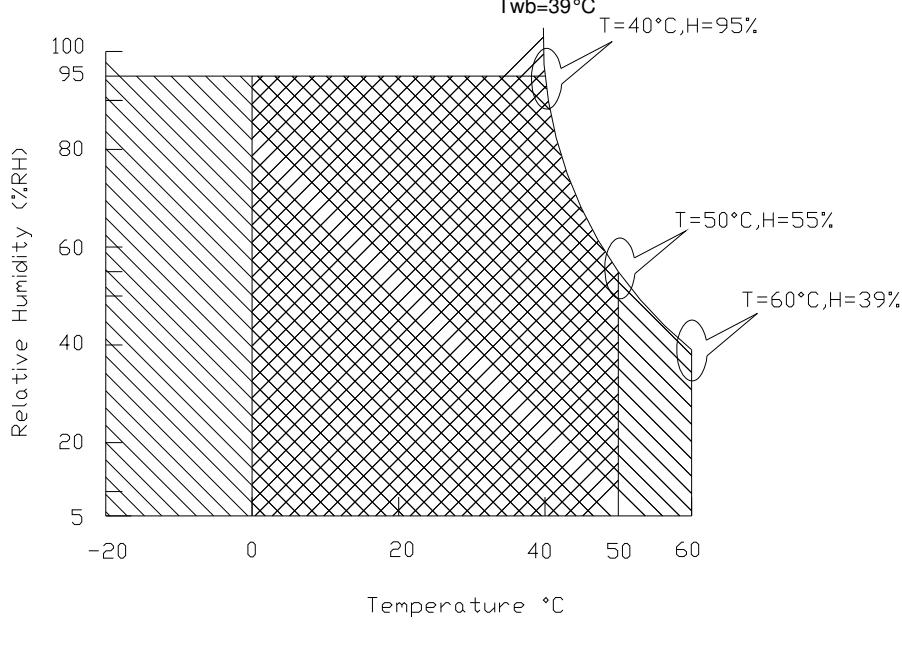
Item	Symbol	Min	Max	Unit	Conditions
Operating	TOP	0	+50	[°C]	Note 4
Operation Humidity	HOP	8	95	[%RH]	Note 4
Storage Temperature	TST	-20	+60	[°C]	Note 4
Storage Humidity	HST	5	95	[%RH]	Note 4

**Note 1:** At Ta (25°C)

**Note 2:** Permanent damage to the device may occur if exceed maximum values

**Note 3:** LED specification refer to section 5.2

**Note 4:** For quality performance, please refer to AUO IIS (Incoming Inspection Standard)



Operating Range

Storage Range

+



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## 5. Electrical Characteristics

### 5.1 TFT LCD Module

#### 5.1.1 Power Specification

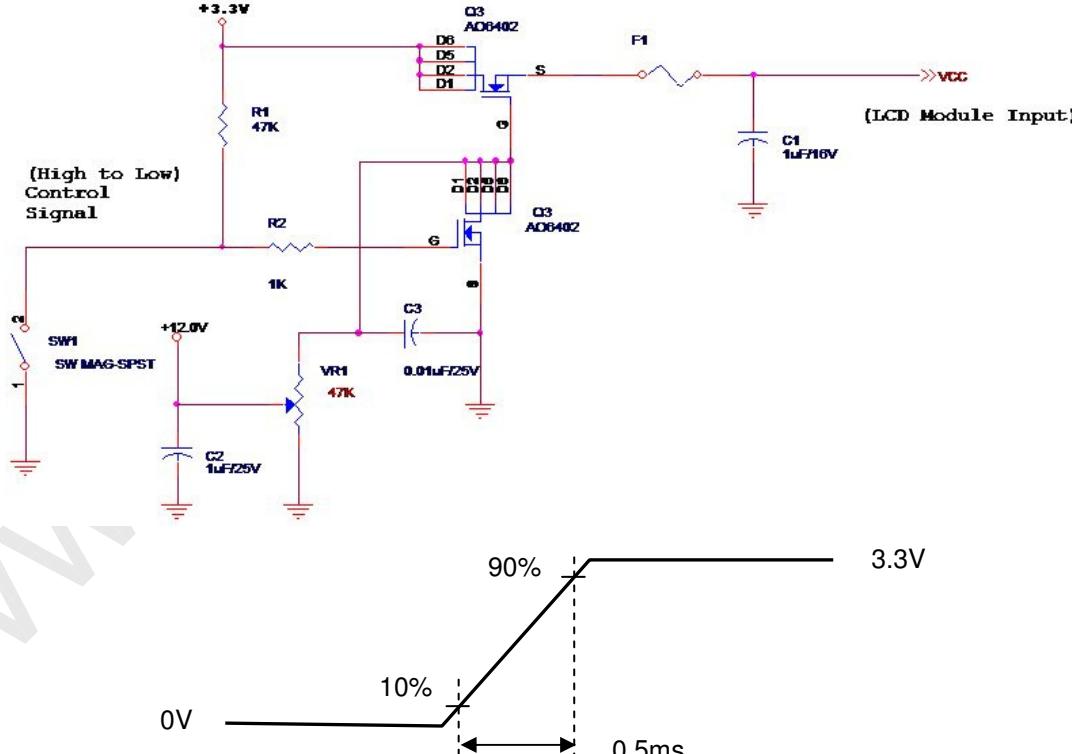
Input power specifications are as follows:

The power specification are measured under 25°C and frame frequency under 60Hz

Symbol	Parameter	Min	Typ	Max	Units	Note
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	
PDD	VDD Power	-	-	2.0	[Watt]	Note 1
IDD	IDD Current	-	-	800	[mA]	Note 1
IRush	Inrush Current	-	-	2000	[mA]	Note 2
VDDRp	Allowable Logic/LCD Drive Ripple Voltage	-	-	100	[mV] p-p	

**Note 1:** Maximum Measurement Condition : White Pattern w/ 3D at 3.3V driving voltage. ( $P_{max} = V_{3.3} \times I_{black}$ )

**Note 2:** Measure Condition





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## 5.1.2 Signal Electrical Characteristics

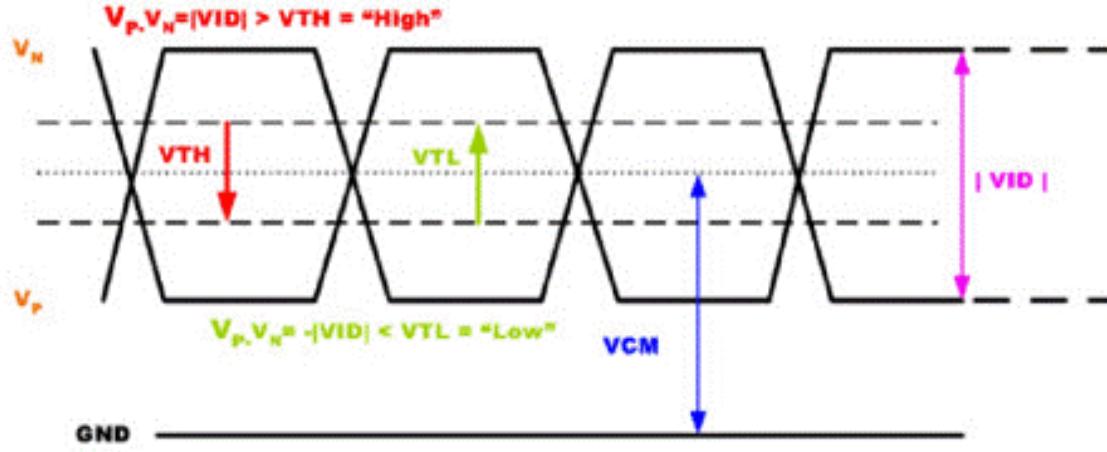
Input signals shall be low or High-impedance state when VDD is off.

Signal electrical characteristics are as follows;

Parameter	Condition	Min	Max	Unit
$V_{TH}$	Differential Input High Threshold ( $V_{cm}=+1.2V$ )	---	100	[mV]
$V_{TL}$	Differential Input Low Threshold ( $V_{cm}=+1.2V$ )	-100	---	[mV]
$ V_{ID} $	Differential Input Voltage	100	600	[mV]
$V_{CM}$	Differential Input Common Mode Voltage	1.125	1.375	[V]

**Note 1:** LVDS Signal Waveform

### Single-end Signal





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## 5.2 Backlight Unit

### 5.2.1 LED characteristics

Parameter	Symbol	Min	Typ	Max	Units	Condition
Backlight Power Consumption	P <sub>LED</sub>	-	-	5W	[Watt]	(Ta=25°C), Note 1
LED Life-Time	N/A	10,000	-	-	Hour	(Ta=25°C), Note 2

**Note 1:** Calculator value for reference  $P_{LED} = VF \text{ (Normal Distribution)} * IF \text{ (Normal Distribution)} / \text{Efficiency}$

**Note 2:** The LED life-time define as the estimated time to 50% degradation of initial luminous.

### 5.2.2 Backlight input signal characteristics

Parameter	Symbol	Min	Typ	Max	Units	Remark
LED Power Supply	V <sub>LED</sub>	6.0	12.0	21.0	[Volt]	Define as Connector Interface (Ta=25°C)
LED Enable Input High Level	V <sub>LED_EN</sub>	2.5	-	5.5	[Volt]	
LED Enable Input Low Level		-	-	0.8	[Volt]	
PWM Logic Input High Level	V <sub>PWM_EN</sub>	2.5	-	5.5	[Volt]	Define as Connector Interface (Ta=25°C)
PWM Logic Input Low Level		-	-	0.8	[Volt]	
PWM Input Frequency	F <sub>PWM</sub>	200	1K	10K	Hz	
PWM Duty Ratio	Duty	5	--	100	%	



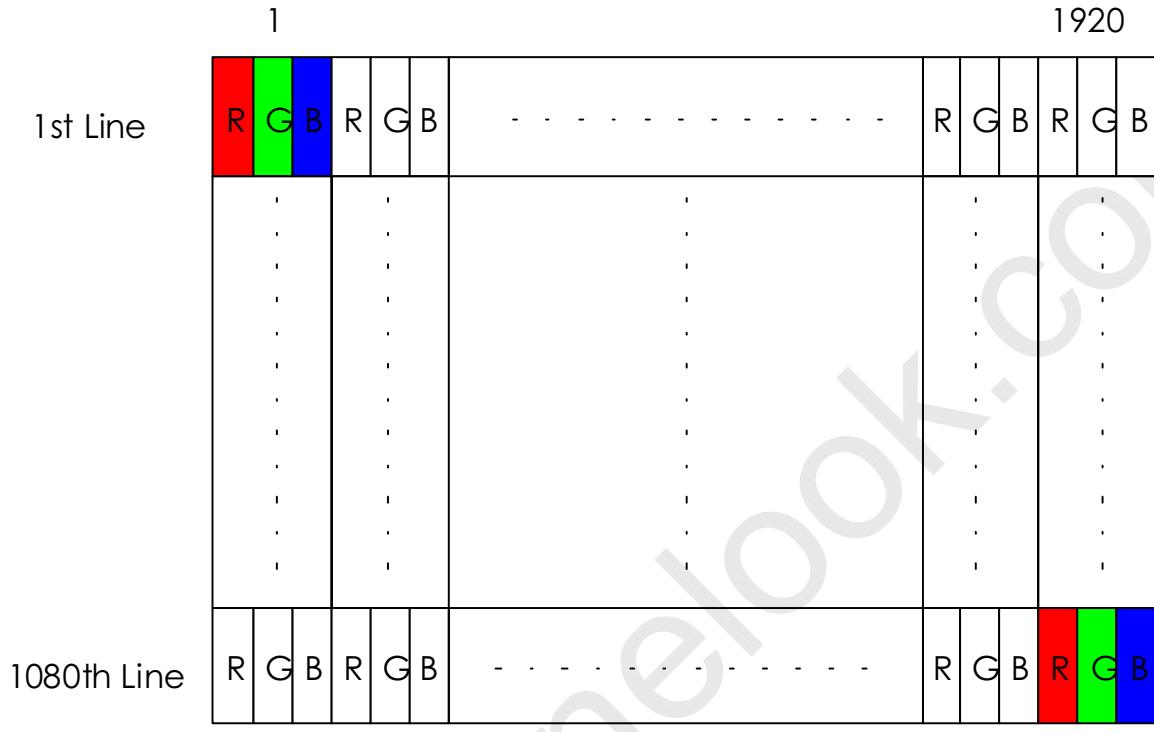
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## 6. Signal Interface Characteristic

### 6.1 Pixel Format Image

Following figure shows the relationship of the input signals and LCD pixel format.

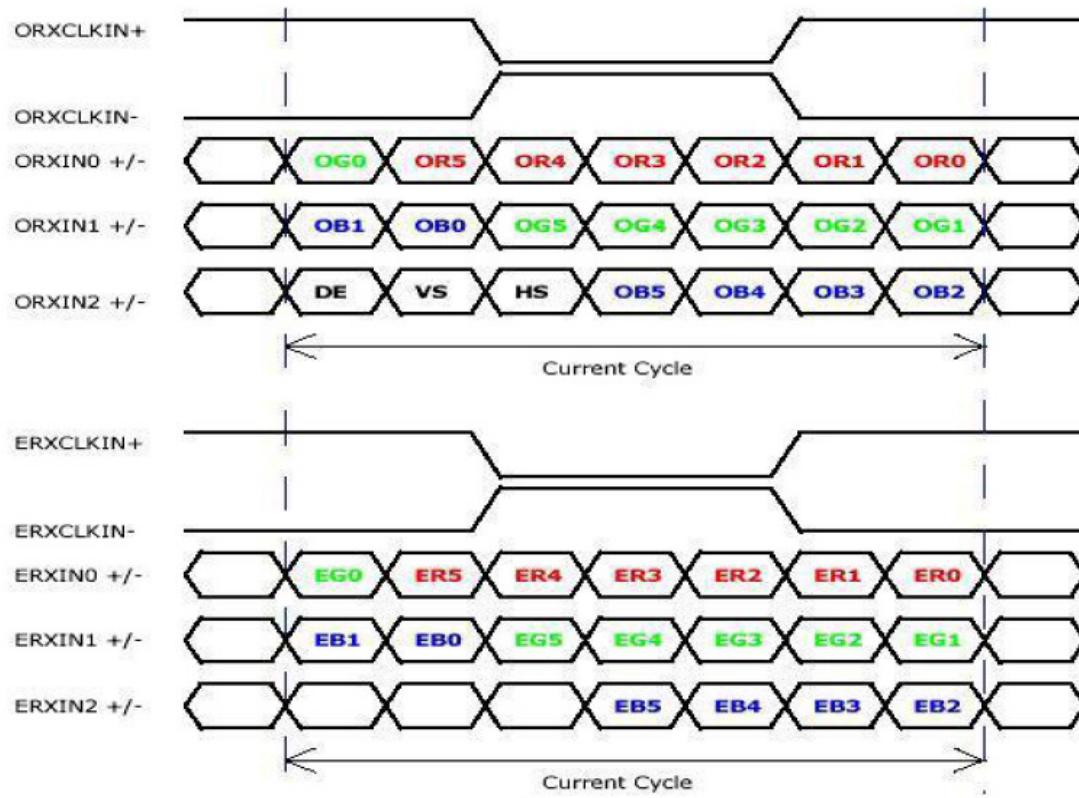




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## 6.2 The Input Data Format



Signal Name	Description	
R5 R4 R3 R2 R1 R0	Red Data 5 (MSB) Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 6 bits pixel data. Red-pixel Data
G5 G4 G3 G2 G1 G0	Green Data 5 (MSB) Green Data 4 Green Data 3 Green Data 2 Green Data 1 Green Data 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 6 bits pixel data. Green-pixel Data
B5 B4 B3 B2 B1 B0	Blue Data 5 (MSB) Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 6 bits pixel data. Blue-pixel Data
RxCLKIN	Data Clock	The signal is used to strobe the pixel data and DE signals. All pixel data shall be valid at the falling edge when the DE signal is high
DE	Display Timing	This signal is strobed at the falling edge of RxCLKIN. When the signal is high, the pixel data shall be valid to be displayed.
VS	Vertical Sync	The signal is synchronized to RxCLKIN .
HS	Horizontal Sync	The signal is synchronized to RxCLKIN .

**Note:** Output signals from any system shall be low or High-impedance state when VDD is off.



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## 6.3 Integration Interface and Pin Assignment

### 6.3.1 Connector Description

Physical interface is described as for the connector on module.

These connectors are capable of accommodating the following signals and will be following components.

Connector Name / Designation	For Signal Connector
Manufacturer	IPEX or compatible
Type / Part Number	20455-040E-12R or compatible
Mating Housing/Part Number	IPEX 20353-040T-11 or compatible

### 6.3.2 Pin Assignment

LVDS is a differential signal technology for LCD interface and high speed data transfer device.

Pin	Signal	Description
1	Reserved	Reserved, AUO will use this pin.
2	VDD	Power Supply, 3.3 V (typical)
3	VDD	Power Supply, 3.3 V (typical)
4	V EEDID	DDC 3.3V power
5	TEST	Panel Self Test
6	Clk EEDID	DDC Clock
7	DATA EEDID	DDC Data
8	Odd_Rin0-	- LVDS differential data input (R0-R5, G0) (odd pixels)
9	Odd_Rin0+	+ LVDS differential data input (R0-R5, G0) (odd pixels)
10	VSS	Ground – Shield
11	Odd_Rin1-	- LVDS differential data input (G1-G5, B0-B1) (odd pixels)
12	Odd_Rin1+	+ LVDS differential data input (G1-G5, B0-B1) (odd pixels)
13	VSS	Ground – Shield
14	Odd_Rin2-	- LVDS differential data input (B2-B5, HS, VS, DE) (odd pixels)
15	Odd_Rin2+	+ LVDS differential data input (B2-B5, HS, VS, DE) (odd pixels)
16	VSS	Ground – Shield
17	Odd_ClkIN-	- LVDS differential clock input (odd pixels)
18	Odd_ClkIN+	+ LVDS differential clock input (odd pixels)
19	VSS	Ground – Shield
20	Even_Rin0-	- LVDS differential data input (R0-R5, G0) (even pixels)
21	Even_Rin0+	+ LVDS differential data input (R0-R5, G0) (even pixels)
22	VSS	Ground – Shield



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23	Even_Rin1-	- LVDS differential data input (G1-G5, B0-B1) (even pixels)
24	Even_Rin1+	+ LVDS differential data input (G1-G5, B0-B1) (even pixels)
25	VSS	Ground – Shield
26	Even_Rin2-	- LVDS differential data input (B2-B5, HS, VS, DE) (even pixels)
27	Even_Rin2+	+ LVDS differential data input (B2-B5, HS, VS, DE) (even pixels)
28	VSS	Ground – Shield
29	Even_ClkIN-	- LVDS differential clock input (even pixels)
30	Even_ClkIN+	+ LVDS differential clock input (even pixels)
31	VSS_LED	Ground – LED
32	DM	USB D-
33	DP	USB D+
34	NC	No connection (Reserved)
35	PWM	System PWM Signal Input (+3.3V Swing)
36	LED_EN	LED enable pin (+3.3V Input)
37	Reserved	Reserved, AUO will use this pin.
38	VDDLED	LED Power Supply
39	VDDLED	LED Power Supply
40	VDDLED	LED Power Supply

**Note 1:** Start from right side



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## 6.4 Interface Timing

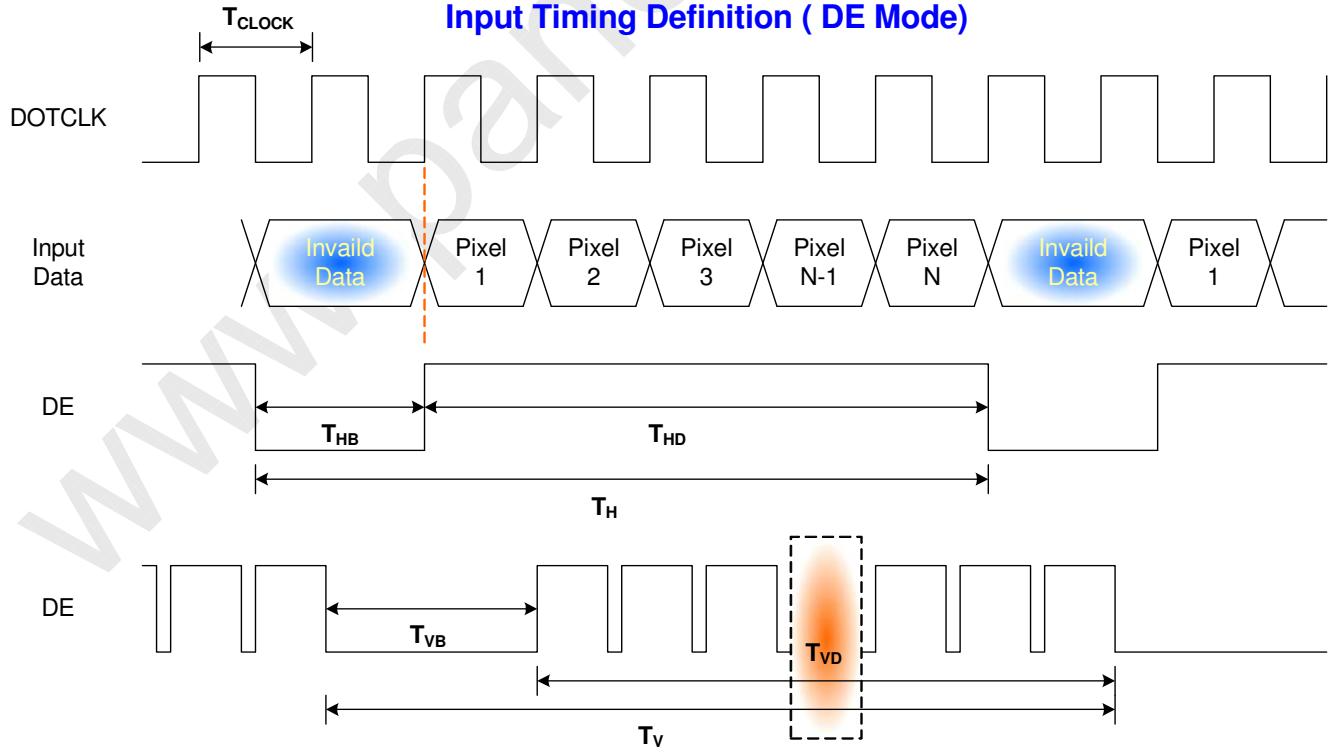
### 6.4.1 Timing Characteristics

Basically, interface timings should match the 1920x1080/ 60Hz manufacturing guide line timing.

Parameter	Symbol	Min.	Typ.	Max.	Unit
Frame Rate	-	40	60	60	Hz
Clock frequency	$1 / T_{Clock}$	68.378	68.45	72.885	MHz
Vertical Section	Period	$T_V$	2088	2090	2150
	Active	$T_{VD}$	1920		
	Blanking	$T_{VB}$	168	170	230
Horizontal Section	Period	$T_H$	1090	1090	1130
	Active	$T_{HD}$	1080		
	Blanking	$T_{HB}$	10	10	50

Note : DE mode only

### 6.4.2 Timing Diagram



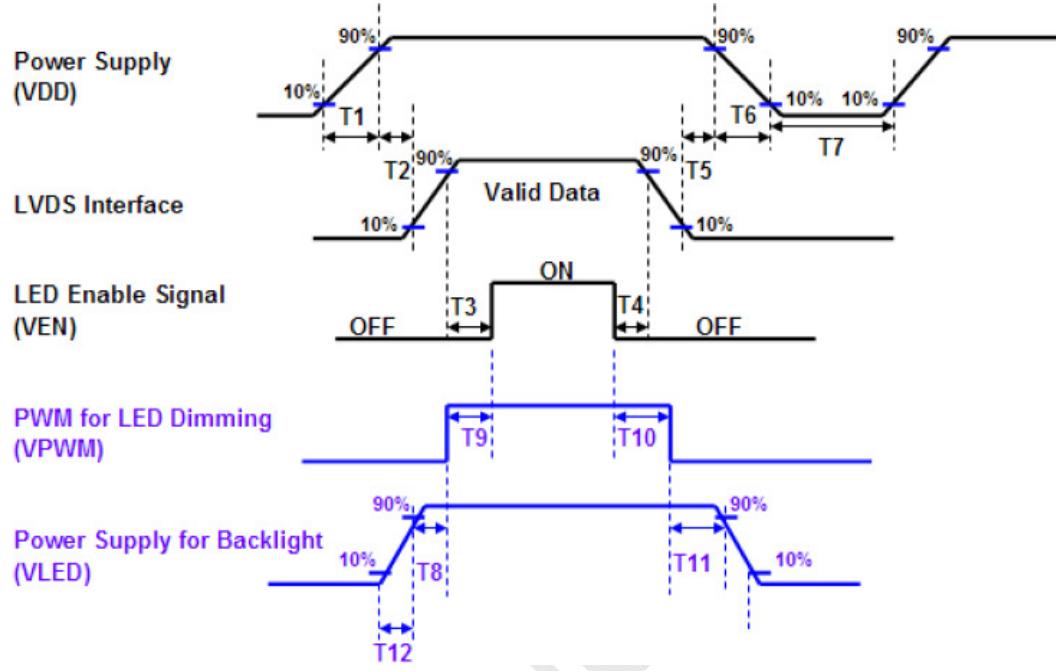


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## **6.5 Power ON/OFF Sequence**

LED on/off sequence is as follows. Interface signals are also shown in the chart.



Power Sequence Timing			
Parameter	Value		Units
	Min.	Max.	
T1	0.5	10	ms
T2	0	50	
T3	400	-	
T4	250	-	
T5	0	50	
T6	0	10	
T7	500	-	
T8	10	-	
T9	10	180	
T10	10	180	
T11	10	-	
T12	0.5	10	

**Note 1:** If  $T4 < 250\text{ms}$  , The display garbage may occur. We suggest  $T4 > 250\text{ms}$  to avoid the display garbage.

**Note 2:** If T1 or T12<0.5ms , the inrush current may cause the damage of fuse. If T1 or T12<0.5ms , the inrush current I<sup>2</sup>t is under typical melt of fuse Spec. , there is no mentioned problem.



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## 7. Panel Reliability Test

### 7.1 Vibration Test

Test Spec:

- Test method: Non-Operation
- Acceleration: 1.5 G
- Frequency: 10 - 500Hz Random
- Sweep: 30 Minutes each Axis (X, Y, Z)

### 7.2 Shock Test

Test Spec:

- Test method: Non-Operation
- Acceleration: 220 G , Half sine wave
- Active time: 2 ms
- Pulse: X,Y,Z one time for each side

### 7.3 Reliability Test

Items	Required Condition	Note
Temperature Humidity Bias	Ta= 40°C, 90%RH, 300h	
High Temperature Operation	Ta= 50°C, Dry, 300h	
Low Temperature Operation	Ta= 0°C, 300h	
High Temperature Storage	Ta= 60°C, 35%RH, 300h	
Low Temperature Storage	Ta= -20°C, 50%RH, 300h	
Thermal Shock Test	Ta=-20°C to 60°C, Duration at 30 min, 100 cycles	
ESD	Contact : ±8 KV Air : ±15 KV	Note 1
Surface Pressure Test(1)	Module surface, test with 196N(20kgf) (φ =16mm). No broken result..	Note2, 3
Surface Pressure Test(2)	Module surface, test with 294N(30kgf) (φ =30mm). No broken result..	Note2, 3

**Note 1:** According to EN 61000-4-2 , ESD class B: Some performance degradation allowed.

Self-recoverable.

No data lost, No hardware failures.

**Note 2:** Test with system cover.

**Note 3:** Static pressure test.

**Remark :** MTBF (Excluding the LED): 30,000 hours with a confidence level 90%

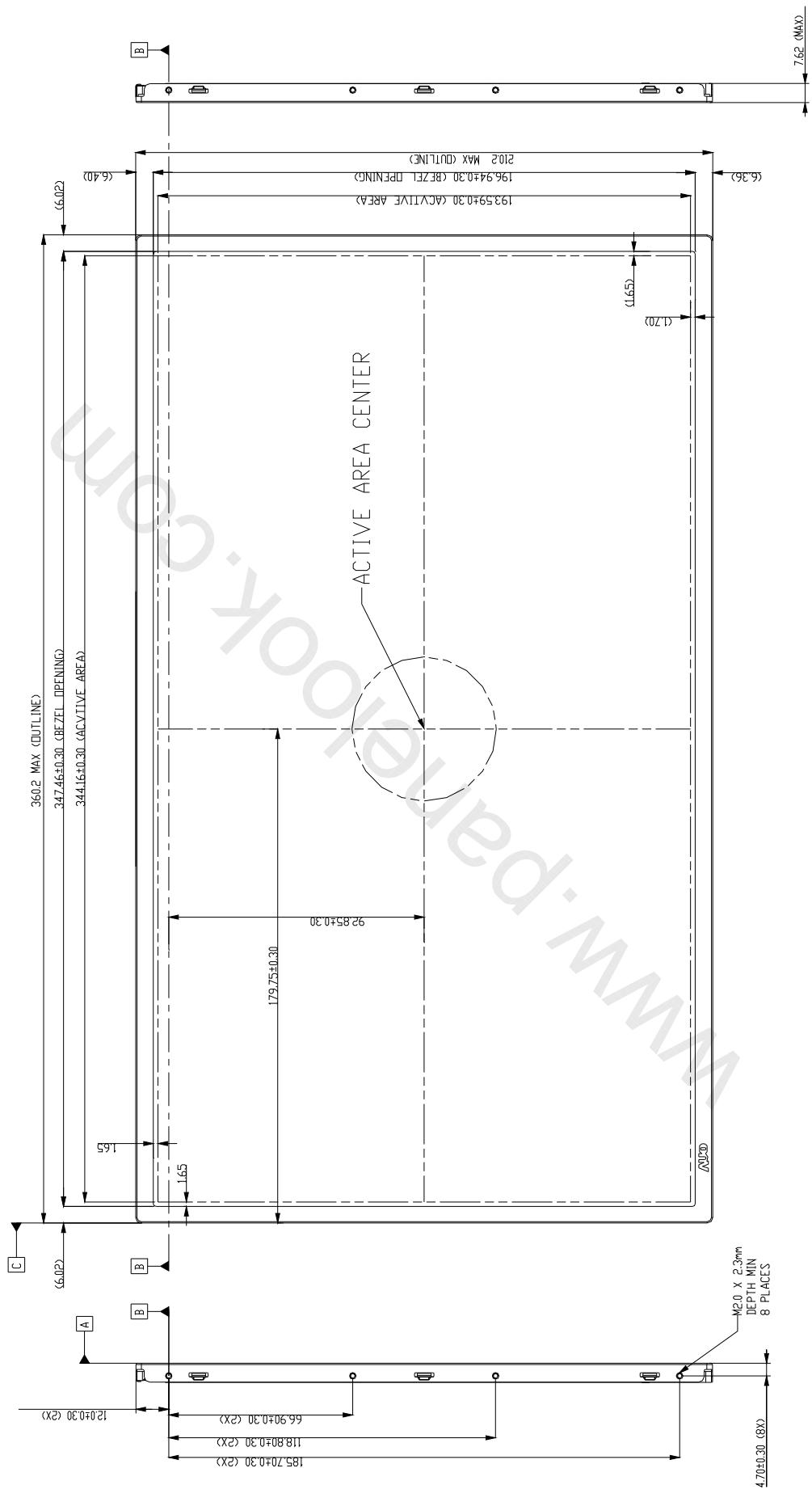


# Product Specification

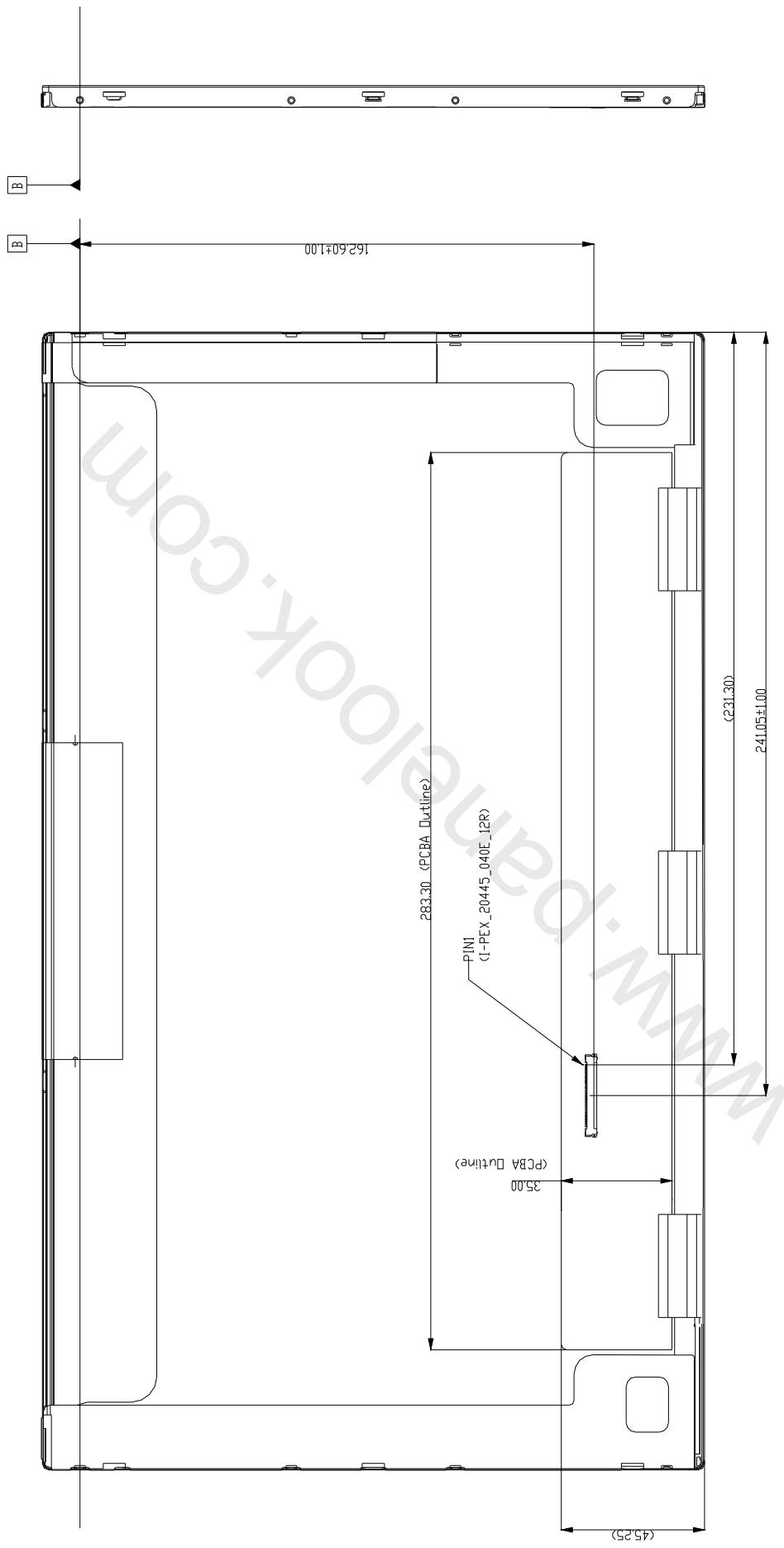
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## 8. Mechanical Characteristics

### 8.1 LCM Outline Dimension (Front View, Screw Hole Depth and Center Position)



## 8.2 LCM Outline Dimension (Back View, PCBA Position)





# Product Specification

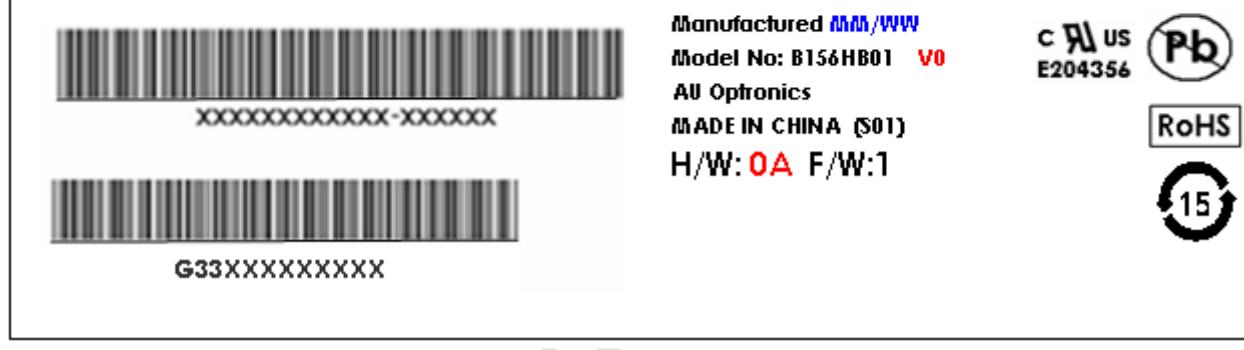
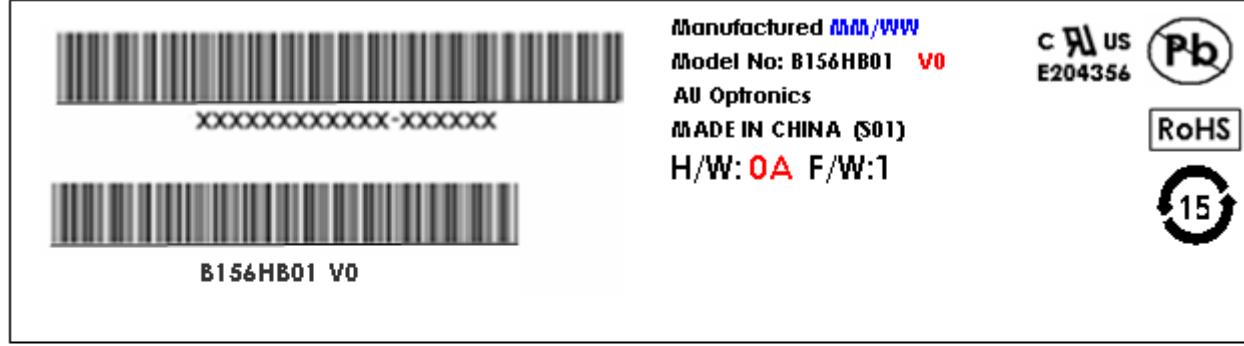
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## 9. Shipping and Package

### 9.1 Shipping Label Format

MADE IN TAIWAN ➔ (M01)

MADE IN CHINA ➔ (S01)



www.panelook.com

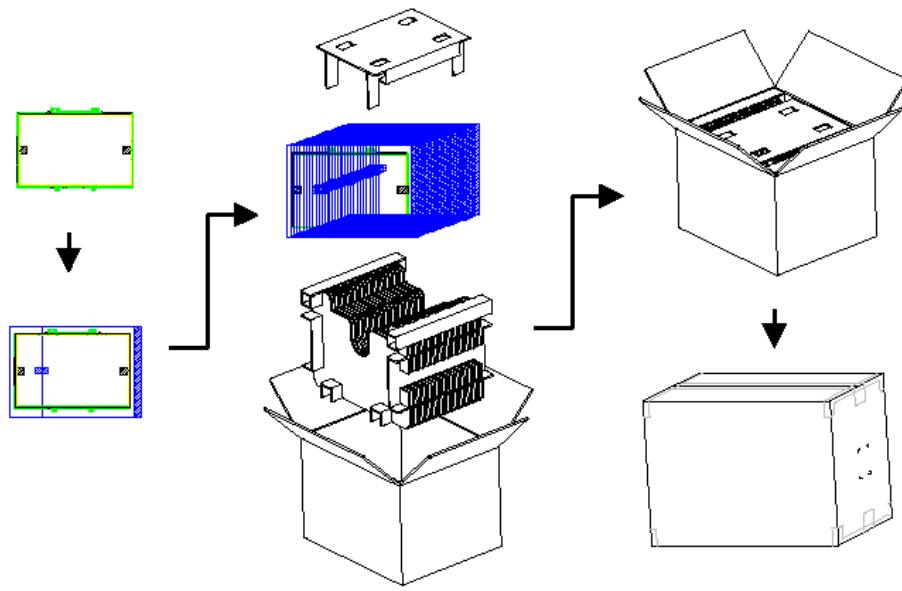


# Product Specification

AU Optronics Corporation

## 9.2 Carton Package

The outside dimension of carton is 439mm\*371mm\*314mm



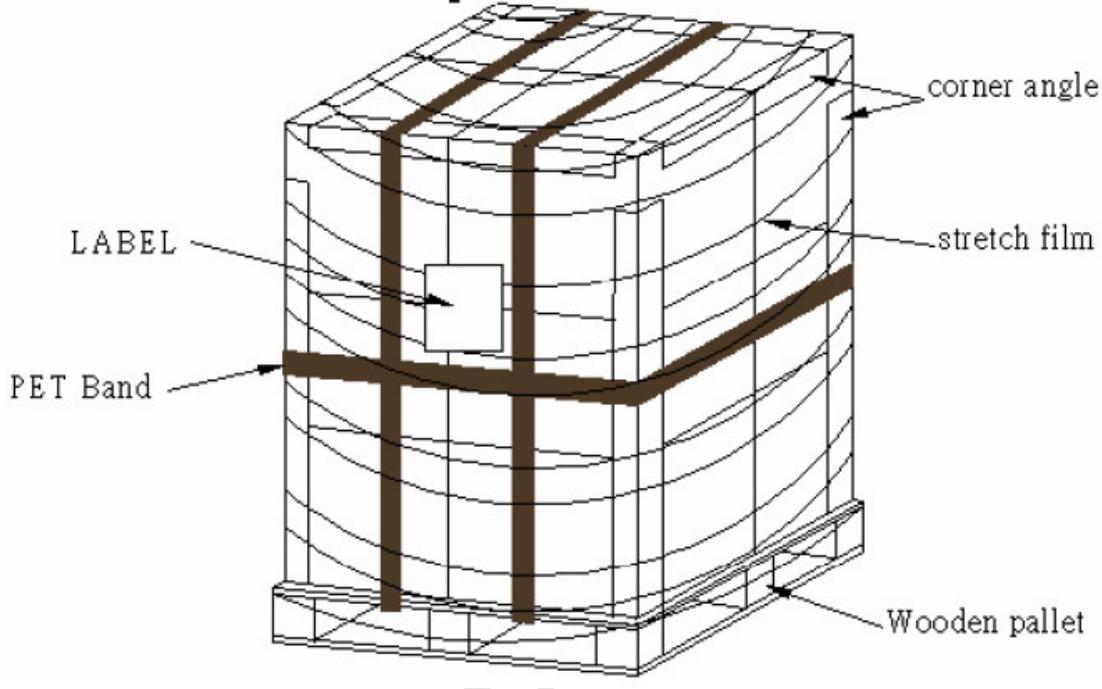
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# Product Specification

AU Optronics Corporation

## 9.3 Shipping Package of Palletizing Sequence



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# Product Specification

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## 10. Appendix

### 10.1 EDID description

Address	FUNCTION	Value HEX	Value BIN	Value DEC	Note
00	Header	00	00000000	0	
01		FF	11111111	255	
02		FF	11111111	255	
03		FF	11111111	255	
04		FF	11111111	255	
05		FF	11111111	255	
06		FF	11111111	255	
07		00	00000000	0	
08	EISA Manuf. Code LSB	06	00000110	6	
09	Compressed ASCII	AF	10101111	175	
0A	Product Code	ED	11101101	237	
0B	hex, LSB first	10	00010000	16	
0C	32-bit ser #	00	00000000	0	
0D		00	00000000	0	
0E		00	00000000	0	
0F		00	00000000	0	
10	Week of manufacture	00	00000000	0	
11	Year of manufacture	14	00010100	20	
12	EDID Structure Ver.	01	00000001	1	
13	EDID revision #	03	00000011	3	
14	<b>Video input def.</b> (digital I/P, non-TMDS, CRGB)	80	10000000	128	
15	<b>Max H image size</b> (rounded to cm)	22	00100010	34	
16	<b>Max V image size</b> (rounded to cm)	13	00010011	19	
17	<b>Display Gamma</b> (=gamma*100)-100	78	01111000	120	
18	<b>Feature support</b> (no DPMS, Active OFF, RGB, tmg Blk#1)	0A	00001010	10	
19	Red/green low bits ( <b>Lower 2:2:2:2 bits</b> )	E2	11100010	226	
1A	Blue/white low bits ( <b>Lower 2:2:2:2 bits</b> )	B5	10110101	181	
1B	Red x ( <b>Upper 8 bits</b> )	9E	10011110	158	
1C	Red y/ highER 8 bits	59	01011001	89	
1D	Green x	52	01010010	82	
1E	Green y	99	10011001	153	
1F	Blue x	26	00100110	38	
20	Blue y	1E	00011110	30	
21	White x	50	01010000	80	
22	White y	54	01010100	84	
23	Established timing 1	00	00000000	0	
24	Established timing 2	00	00000000	0	
25	Established timing 3	00	00000000	0	
26	Standard timing #1	01	00000001	1	
27		01	00000001	1	
28	Standard timing #2	01	00000001	1	



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<b>29</b>		01	00000001	1	
<b>2A</b>	Standard timing #3	01	00000001	1	
<b>2B</b>		01	00000001	1	
<b>2C</b>	Standard timing #4	01	00000001	1	
<b>2D</b>		01	00000001	1	
<b>2E</b>	Standard timing #5	01	00000001	1	
<b>2F</b>		01	00000001	1	
<b>30</b>	Standard timing #6	01	00000001	1	
<b>31</b>		01	00000001	1	
<b>32</b>	Standard timing #7	01	00000001	1	
<b>33</b>		01	00000001	1	
<b>34</b>	Standard timing #8	01	00000001	1	
<b>35</b>		01	00000001	1	
<b>36</b>	Pixel Clock/10000 LSB	7A	01111010	122	
<b>37</b>	Pixel Clock/10000 USB	35	00110101	53	
<b>38</b>	Horz active <b>Lower 8bits</b>	80	10000000	128	
<b>39</b>	Horz blanking <b>Lower 8bits</b>	AA	10101010	170	
<b>3A</b>	HorzAct:HorzBlnk <b>Upper 4:4 bits</b>	70	01110000	112	
<b>3B</b>	Vertical Active <b>Lower 8bits</b>	38	00111000	56	
<b>3C</b>	Vertical Blanking <b>Lower 8bits</b>	0A	00001010	10	
<b>3D</b>	Vert Act : Vertical Blanking <b>(upper 4:4 bit)</b>	40	01000000	64	
<b>3E</b>	HorzSync. Offset	30	00110000	48	
<b>3F</b>	HorzSync.Width	20	00100000	32	
<b>40</b>	VertSync.Offset : VertSync.Width	64	01100100	100	
<b>41</b>	Horz&Vert Sync Offset/Width <b>Upper 2bits</b>	00	00000000	0	
<b>42</b>	Horizontal Image Size <b>Lower 8bits</b>	58	01011000	88	
<b>43</b>	Vertical Image Size <b>Lower 8bits</b>	C1	11000001	193	
<b>44</b>	Horizontal & Vertical Image Size <b>(upper 4:4 bits)</b>	10	00010000	16	
<b>45</b>	Horizontal Border (zero for internal LCD)	00	00000000	0	
<b>46</b>	Vertical Border (zero for internal LCD)	00	00000000	0	
<b>47</b>	Signal (non-intr, norm, no stero, sep sync, neg pol)	18	00011000	24	
<b>48</b>	Detailed timing/monitor	00	00000000	0	
<b>49</b>	descriptor #2	00	00000000	0	
<b>4A</b>		00	00000000	0	
<b>4B</b>		0F	00001111	15	
<b>4C</b>		00	00000000	0	
<b>4D</b>		00	00000000	0	
<b>4E</b>		00	00000000	0	
<b>4F</b>		00	00000000	0	
<b>50</b>		00	00000000	0	
<b>51</b>		00	00000000	0	
<b>52</b>		00	00000000	0	
<b>53</b>		00	00000000	0	
<b>54</b>		00	00000000	0	
<b>55</b>		00	00000000	0	



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56		00	00000000	0	
57		00	00000000	0	
58		00	00000000	0	
59		20	00100000	32	
5A	Detailed timing/monitor	00	00000000	0	
5B	descriptor #3	00	00000000	0	
5C		00	00000000	0	
5D		FE	11111110	254	
5E		00	00000000	0	
5F	Manufacture	41	01000001	65	A
60	Manufacture	55	01010101	85	U
61	Manufacture	4F	01001111	79	O
62		0A	00001010	10	
63		20	00100000	32	
64		20	00100000	32	
65		20	00100000	32	
66		20	00100000	32	
67		20	00100000	32	
68		20	00100000	32	
69		20	00100000	32	
6A		20	00100000	32	
6B		20	00100000	32	
6C	Detailed timing/monitor	00	00000000	0	
6D	descriptor #4	00	00000000	0	
6E		00	00000000	0	
6F		FE	11111110	254	
70		00	00000000	0	
71	Manufacture P/N	42	01000010	66	B
72	Manufacture P/N	31	00110001	49	1
73	Manufacture P/N	35	00110101	53	5
74	Manufacture P/N	36	00110110	54	6
75	Manufacture P/N	48	01001000	72	H
76	Manufacture P/N	42	01000010	66	B
77	Manufacture P/N	30	00110000	48	0
78	Manufacture P/N	31	00110001	49	1
79	Manufacture P/N	20	00100000	32	
7A	Manufacture P/N	56	01010110	86	V
7B	Manufacture P/N	30	00110000	48	0
7C		20	00100000	32	
7D		0A	00001010	10	
7E	Extension Flag	00	00000000	0	
7F	Checksum	01	00000001	1	